

EMC Compo 2019

in Haining, Hangzhou, China



EMC COMPO 2019
21-23, October
Haining, Hangzhou

October 21-23, 2019 Haining, Hangzhou, China

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Call for Papers



The achievement in terms of operating frequency and integration of semiconductor technology are constantly creating new challenges in IC EMC, which must necessarily be addressed at both integrated circuits and system level. Keeping up-to-date is of paramount importance to be successful in this field. Since the inception of the 1st International IC EMC Workshop in Toulouse, France, in 1999, it has been held various at Europe and one time in Japan, **The 12th EMC COMPO will be held in Haining, Hangzhou**, a most advanced electronic industry and research hub located at the Yangtze Delta and Shanghai Industry Zone.

EMC Compo is the first time to be held in China. The workshop will presents the state-of-arts on IC EMC related issues, not only fundamental improvements in modeling and measurement, but also novel design methodologies and standards. A Technical Exhibition will provide tool and equipment manufacturers and suppliers an opportunity to display their products and services to potential clients.

All eligible papers will be submitted for online publication at the IEEE Xplore Digital library. Authors are encouraged to submit extended paper for possible publication in IEEE Trans on EMC. Please prepare come to this unique workshop and conference, presents your latest development, meet international colleagues, learn from experts, find collaborators.

Symposium Topics

- Artificial Intelligence in IC EMC
- Design of 2D and 3D system-on-chip (SoC) for EMC
- Hardware-software co-design and integration for IC EMC
- Emission and immunity-aware IC design
- ESD immunity techniques at IC level
- Signal and power integrities at IC level
- Combined effects of radiation and aging on IC EM sensitivity
- Harsh environment effects on IC EM sensitivity
- IC EMC for avionics and automotive applications
- EMC-aware analog and mixed signal circuits
- RF ICs EMC
- IC-level measurement techniques for EMC
- IC-level modeling techniques for EMC
- EMC simulation of ICs
- EMC in microwave ICs
- EMC-aware software solutions
- FPGA-based embedded systems and EMC

Important Dates

<ul style="list-style-type: none"> ▪ Preliminary paper submission(3 pages in PDF) ▪ Abstract submission (500 words to 1-page) 	Extended to 30 July 2019
<ul style="list-style-type: none"> ▪ Notification of review feedback 	16 Aug. 2019
<ul style="list-style-type: none"> ▪ Final paper Due 	05 Sept. 2019

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